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FIG 1

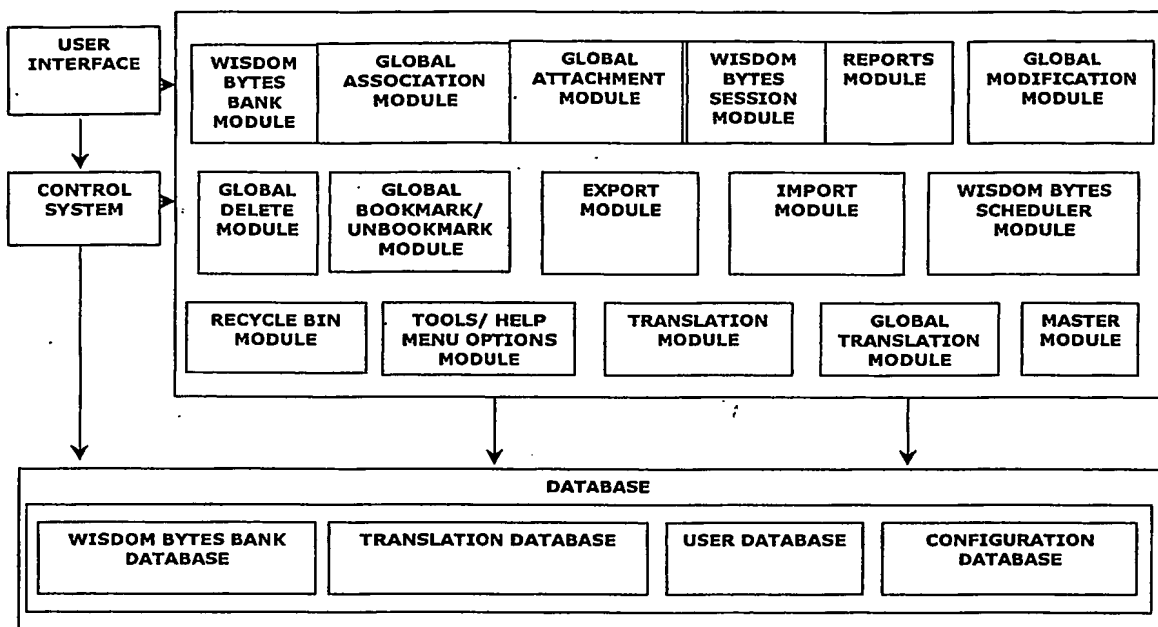
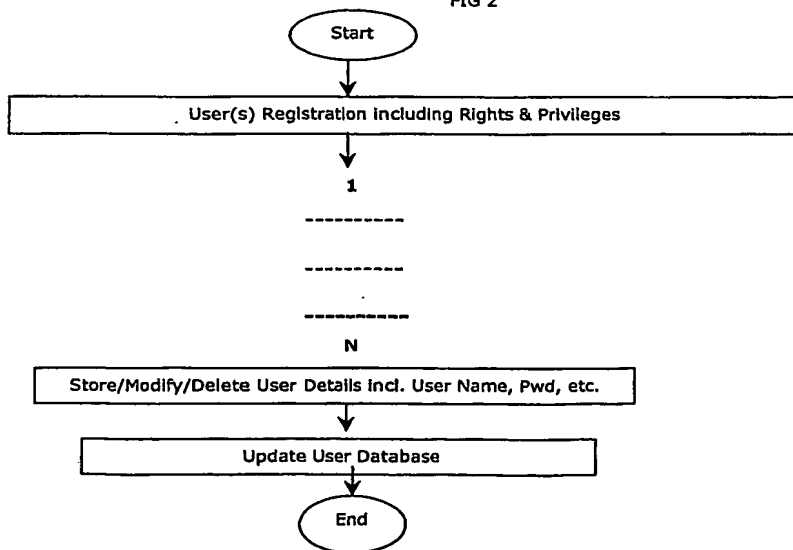
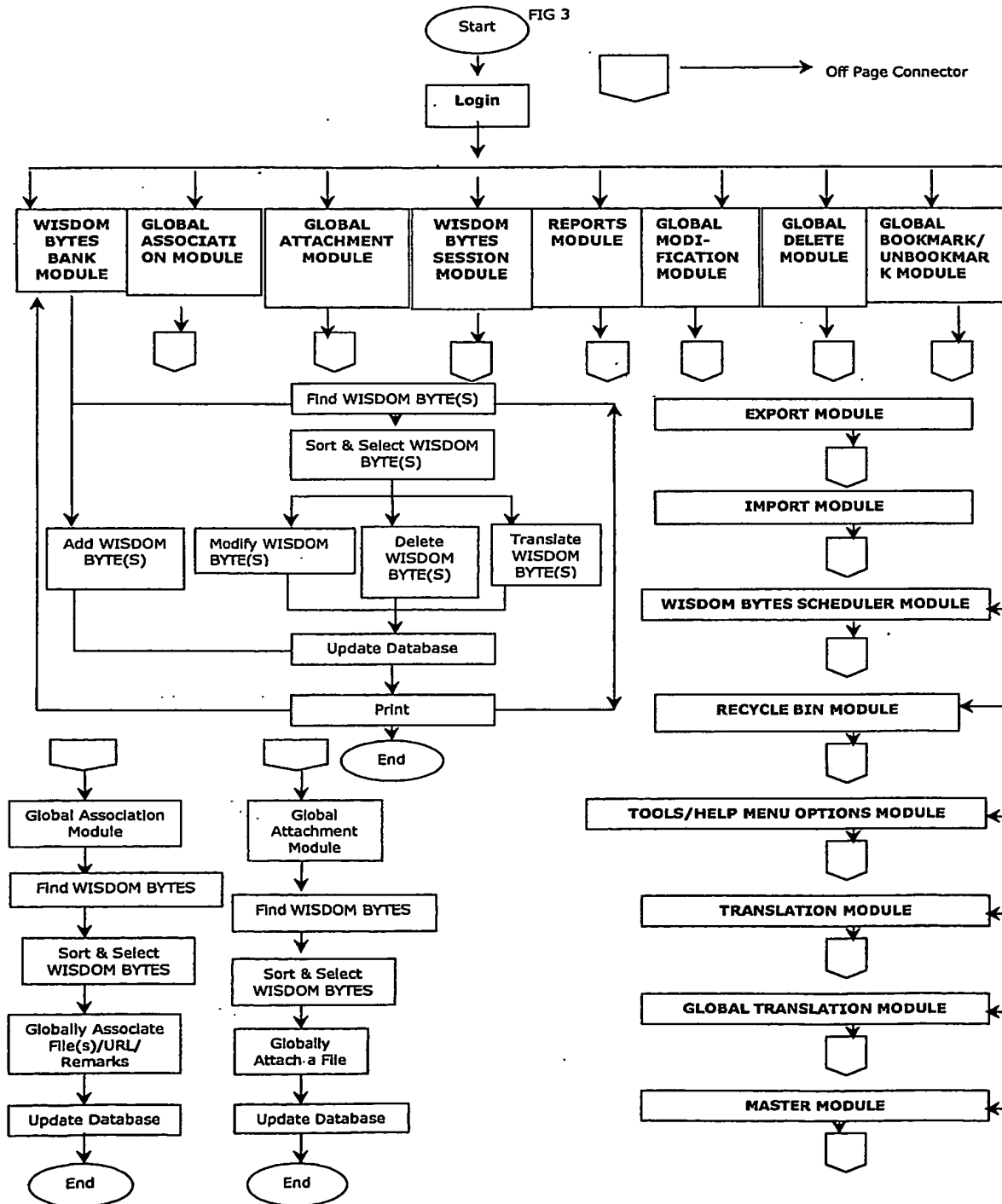


FIG 2

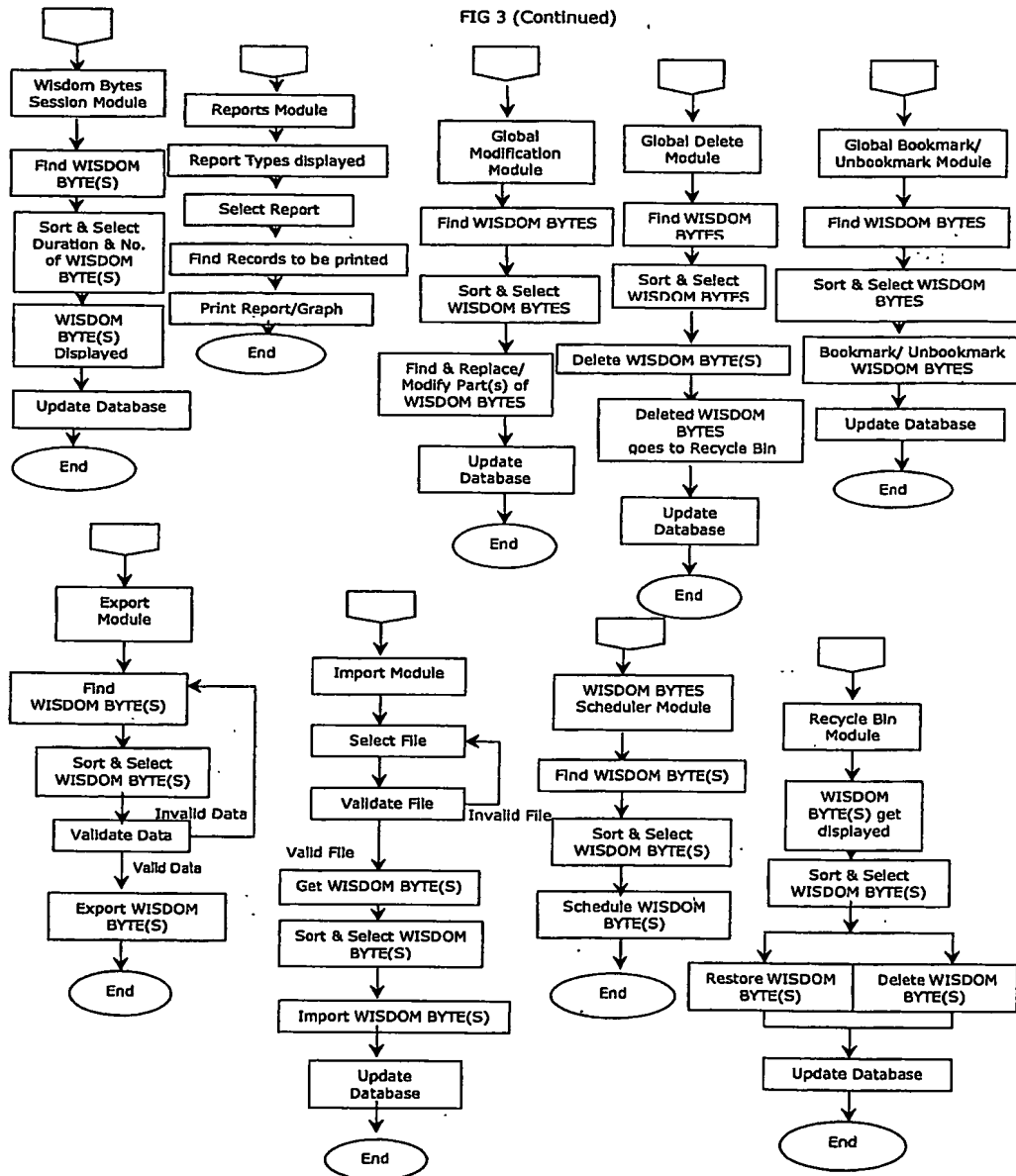


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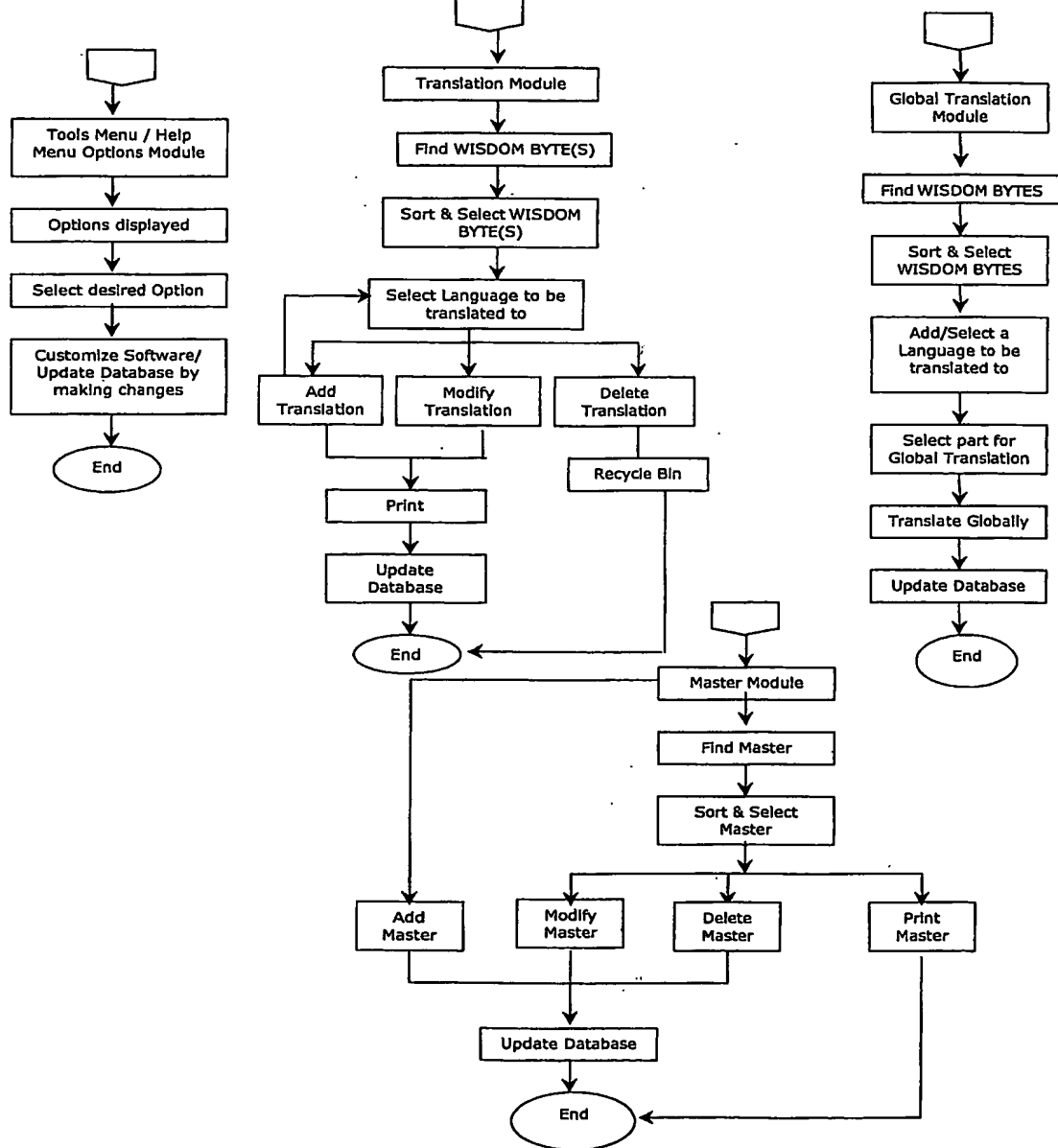
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FIG 3 (Continued)



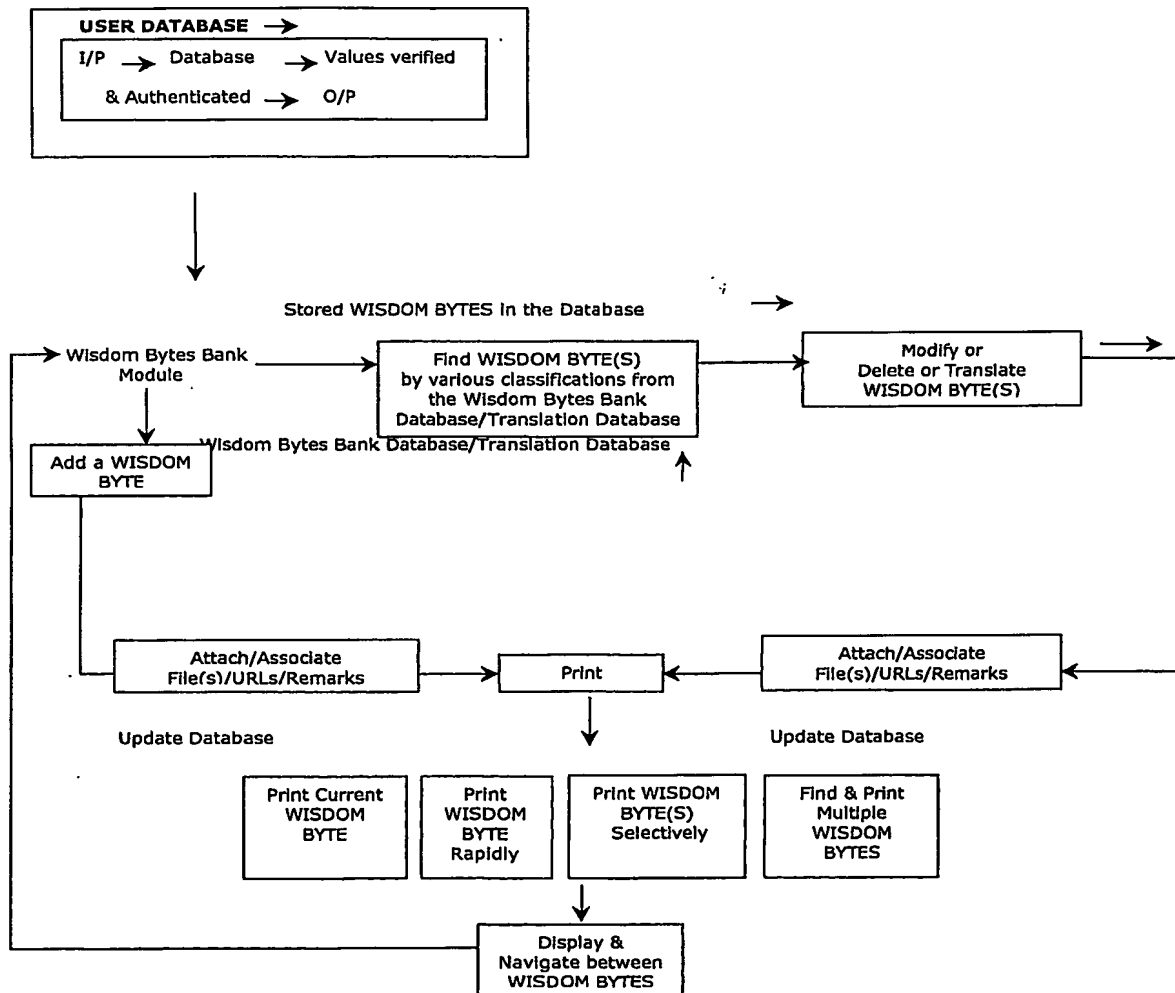
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FIG 3 (Continued)



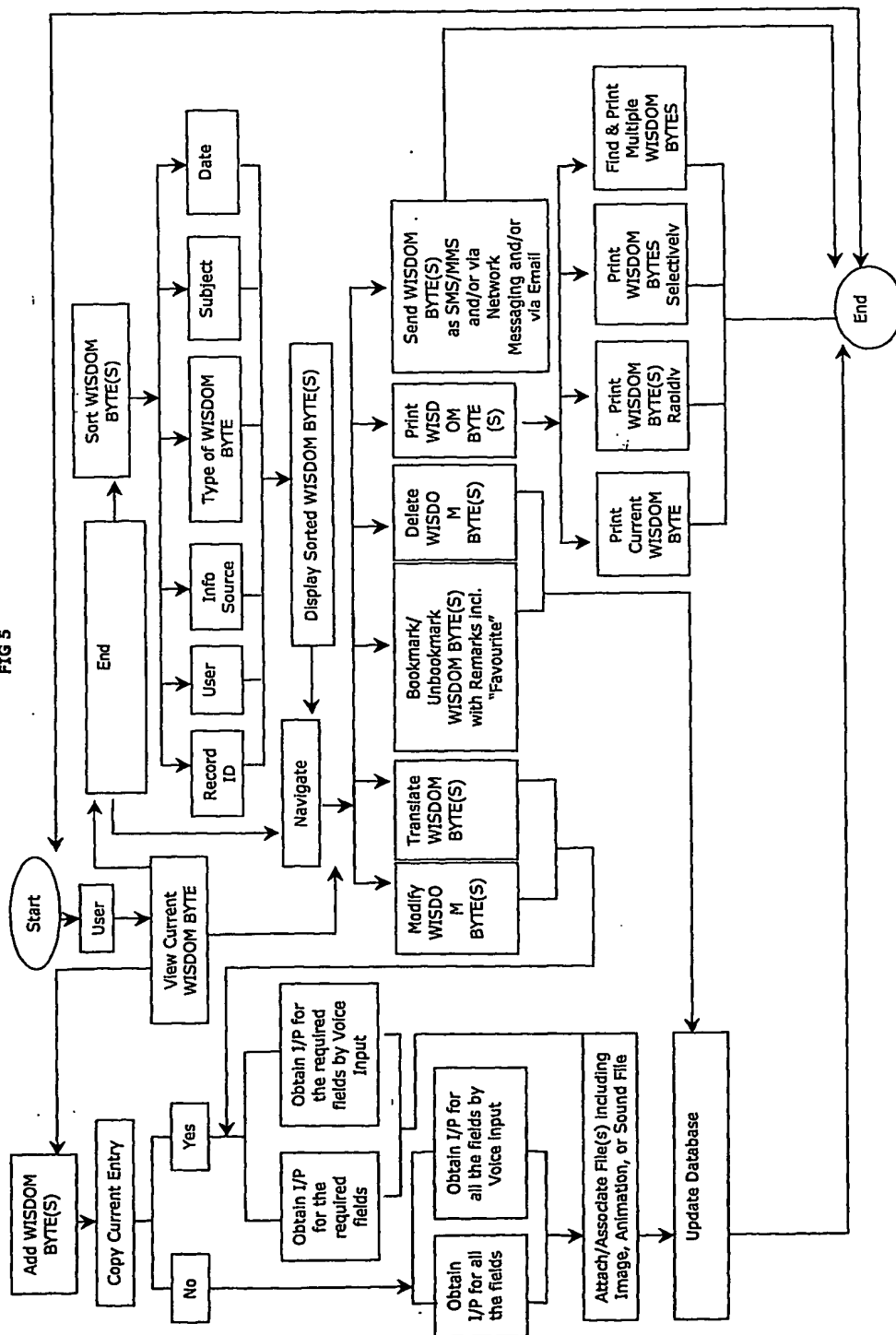
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FIG 4



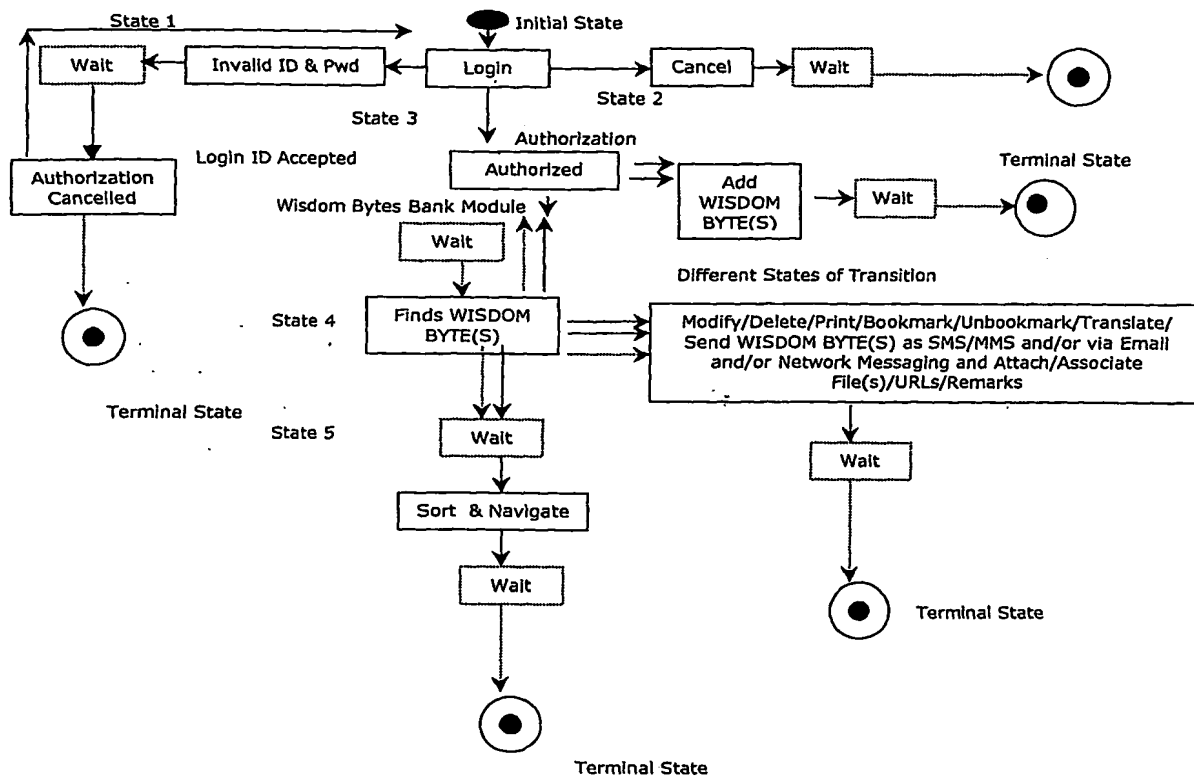
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FIG 5



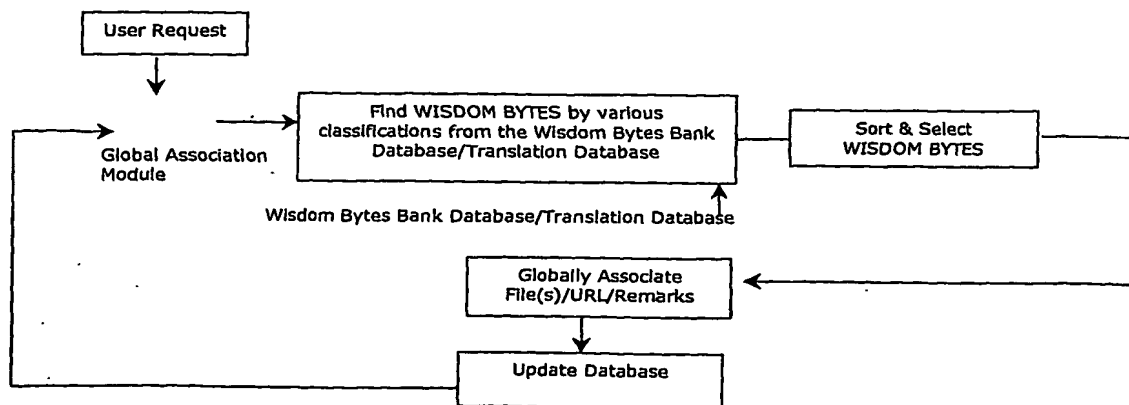
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FIG 6

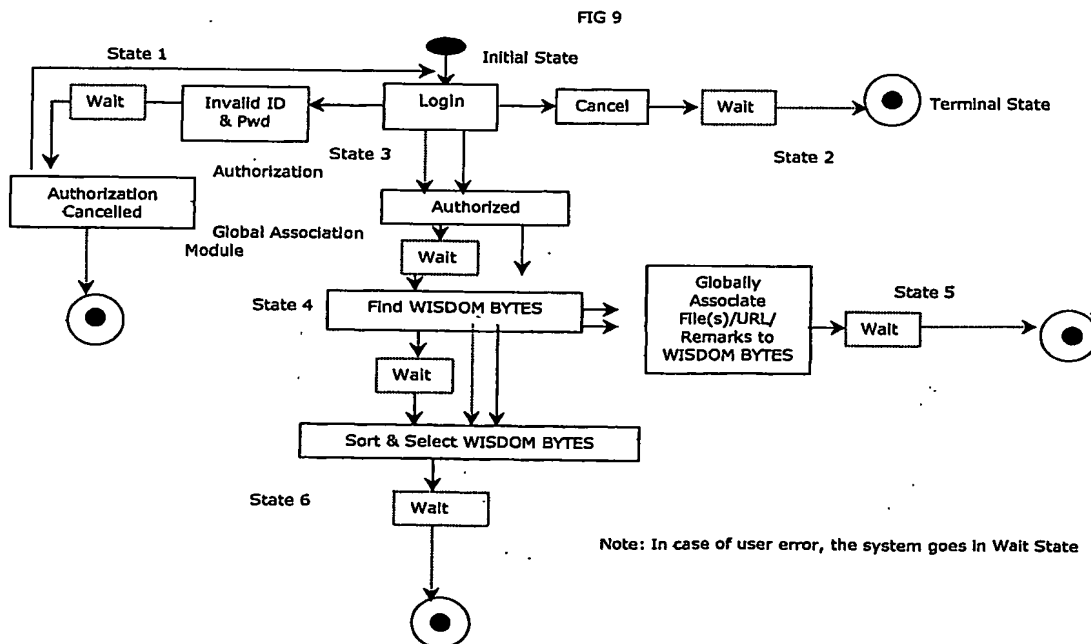
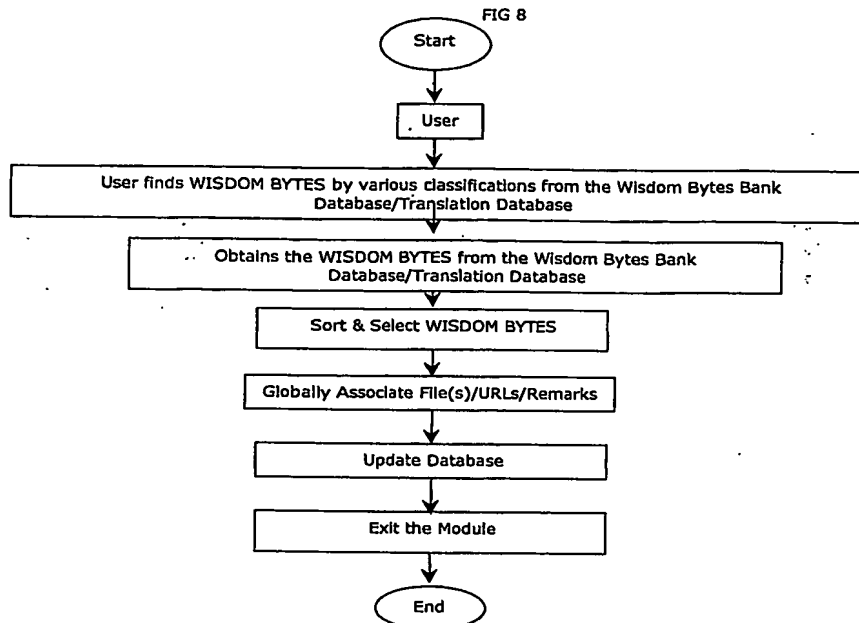


Note: In case of user error, the system goes in Wait State.

FIG 7



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FIG 10

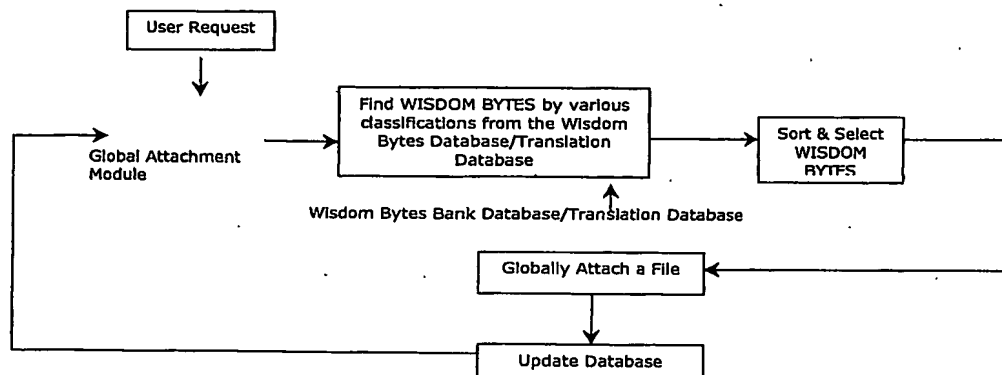
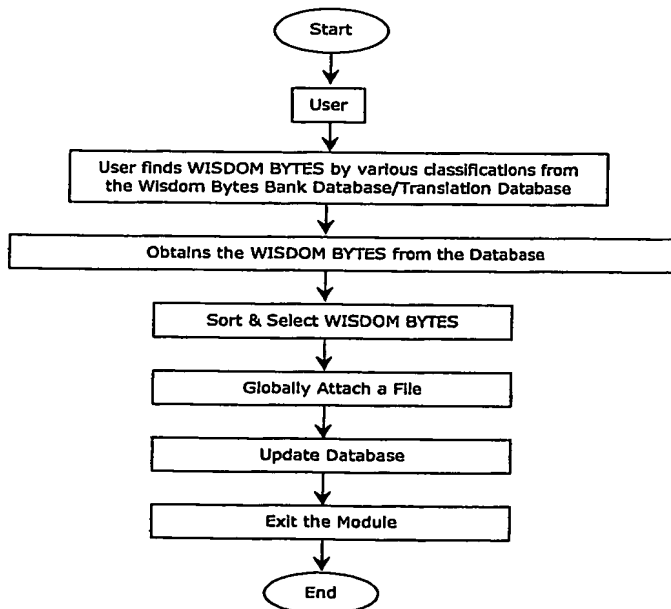


FIG 11



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FIG 12

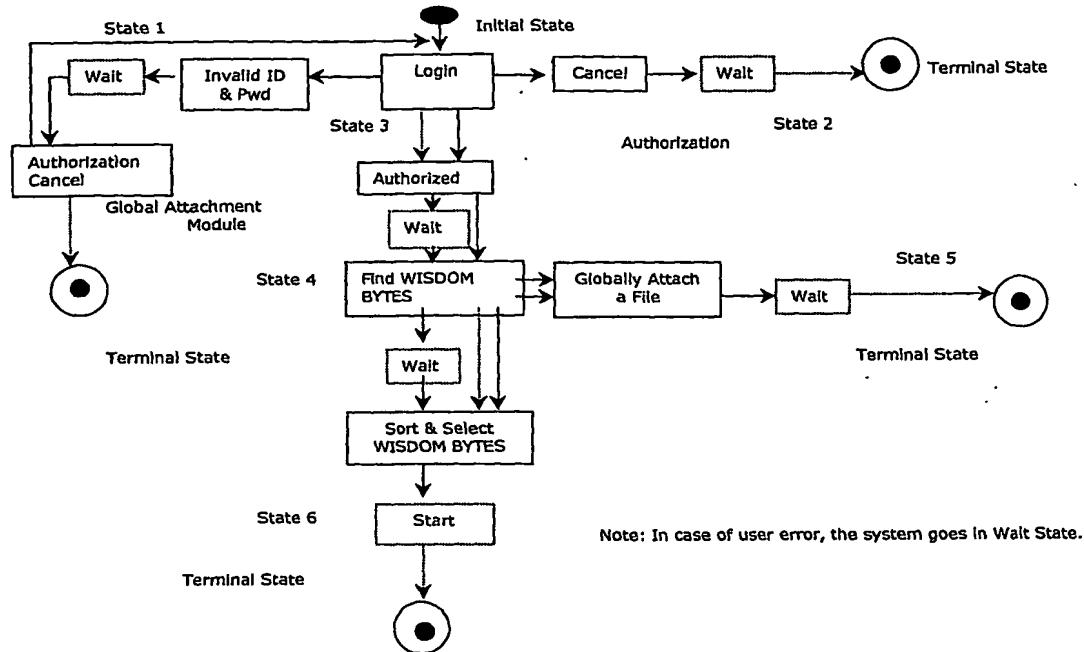
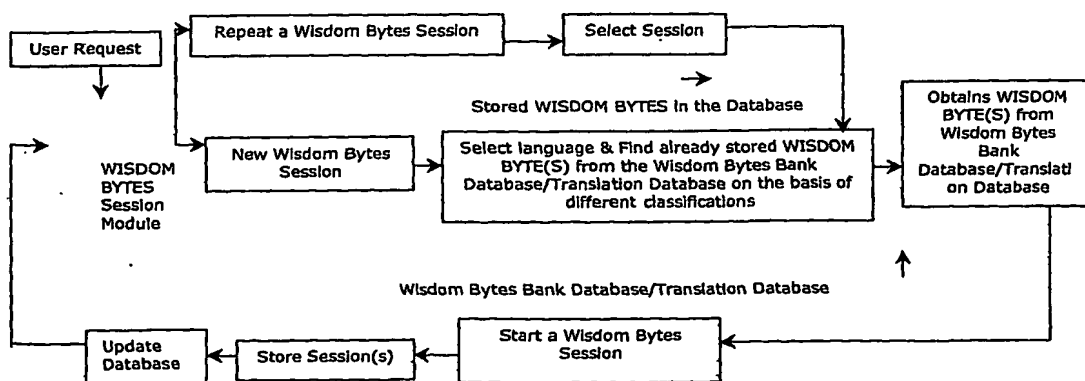
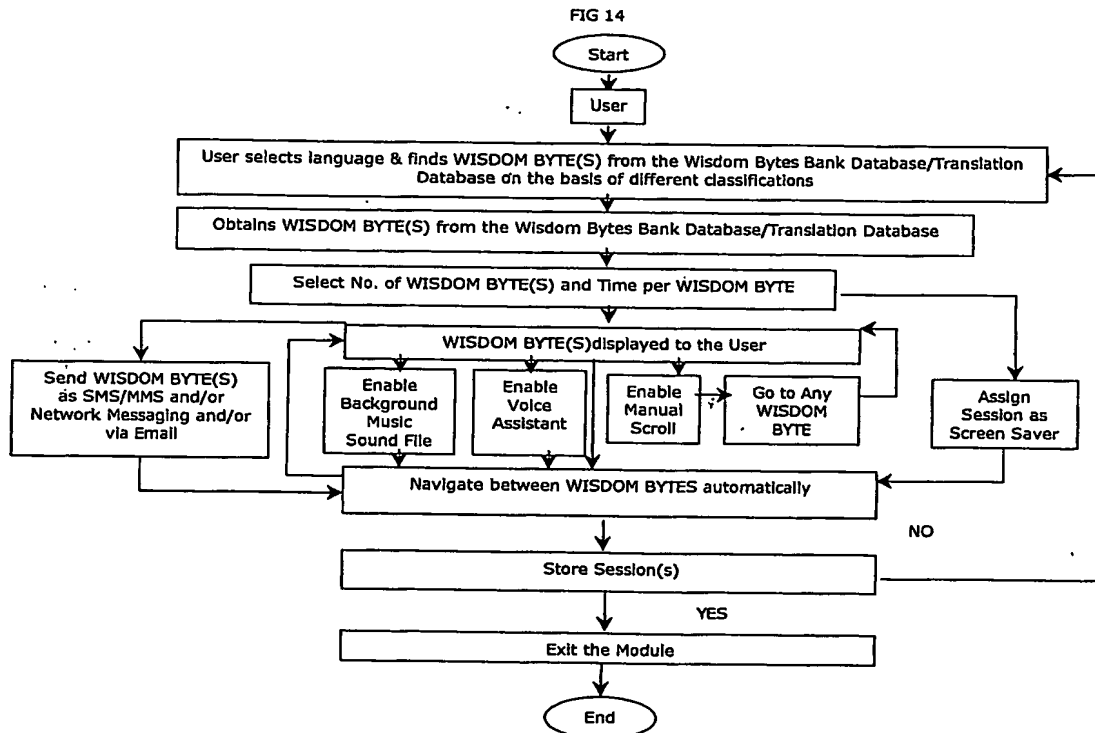


FIG 13



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FIG 14



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FIG 15

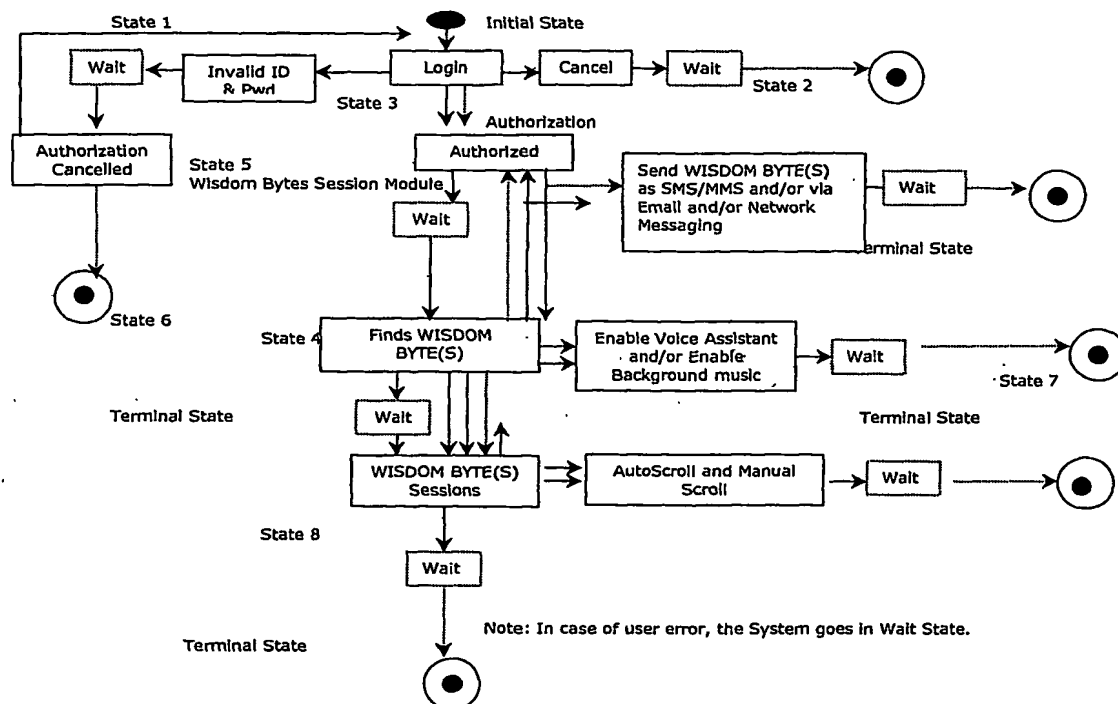
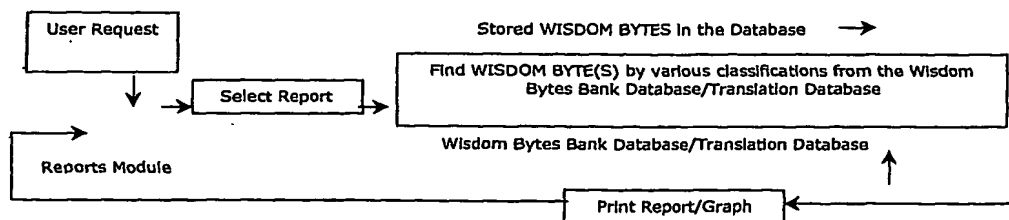
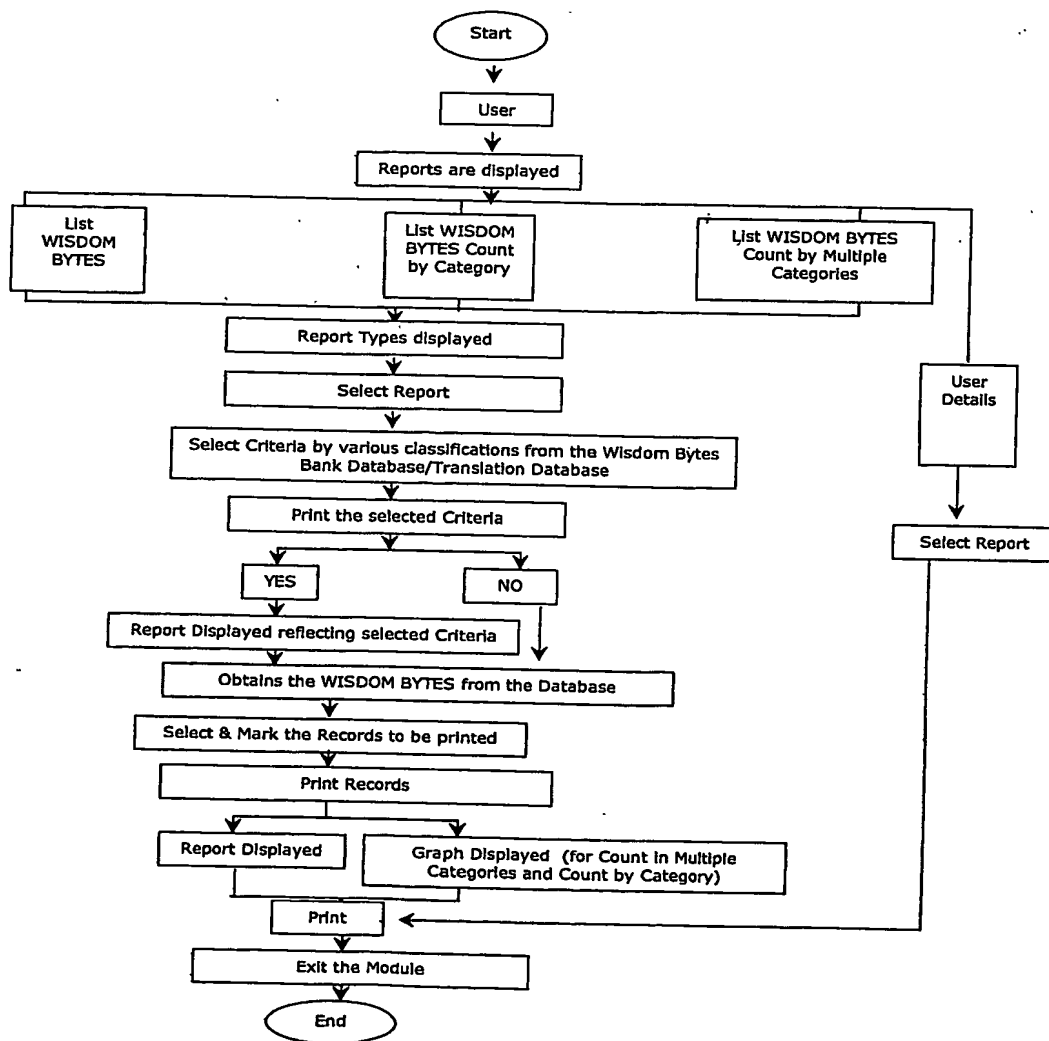


FIG 16

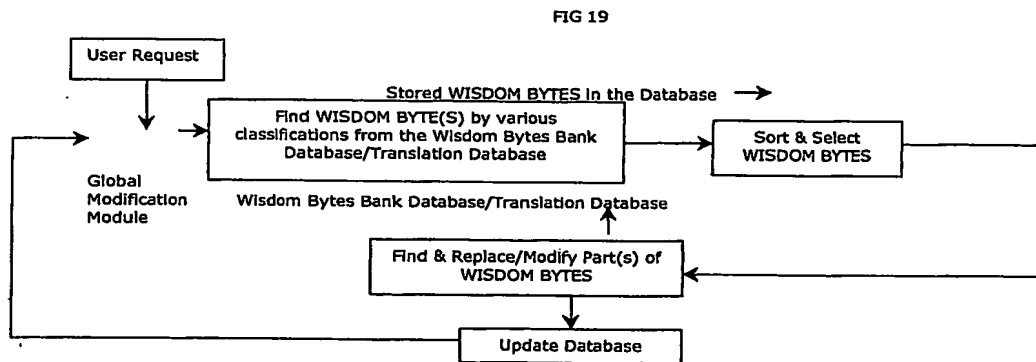
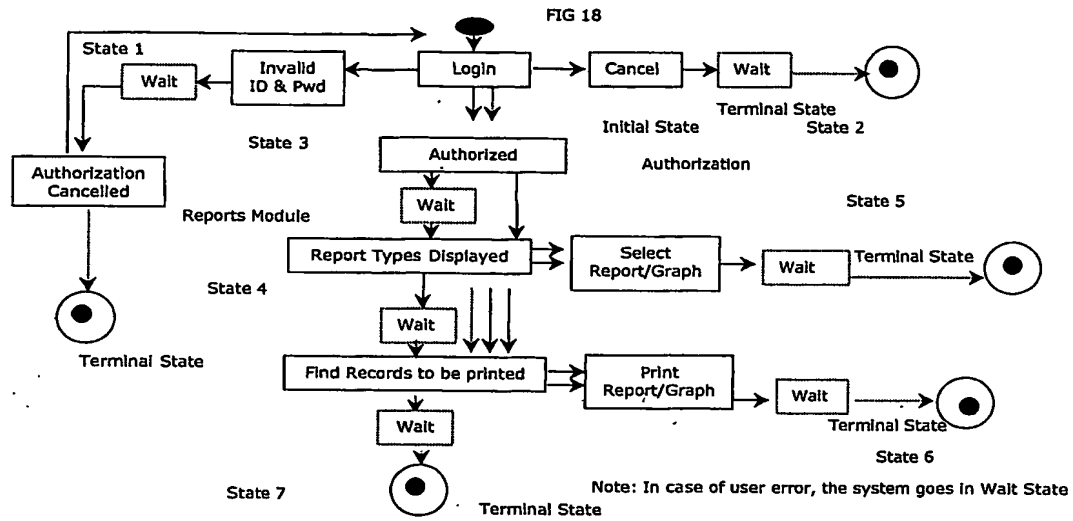


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FIG 17



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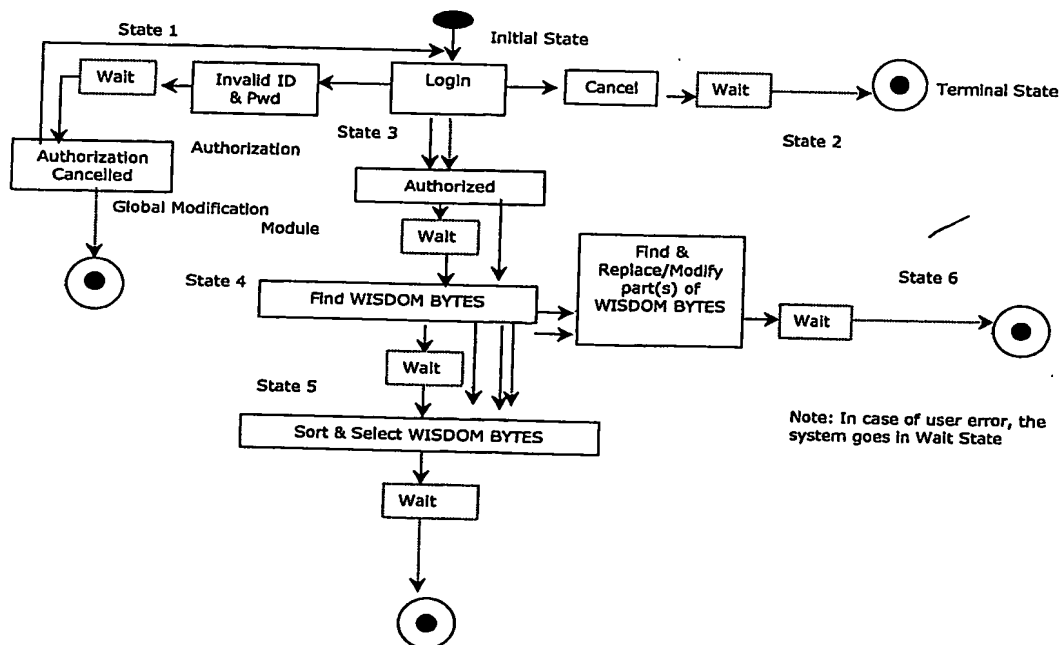
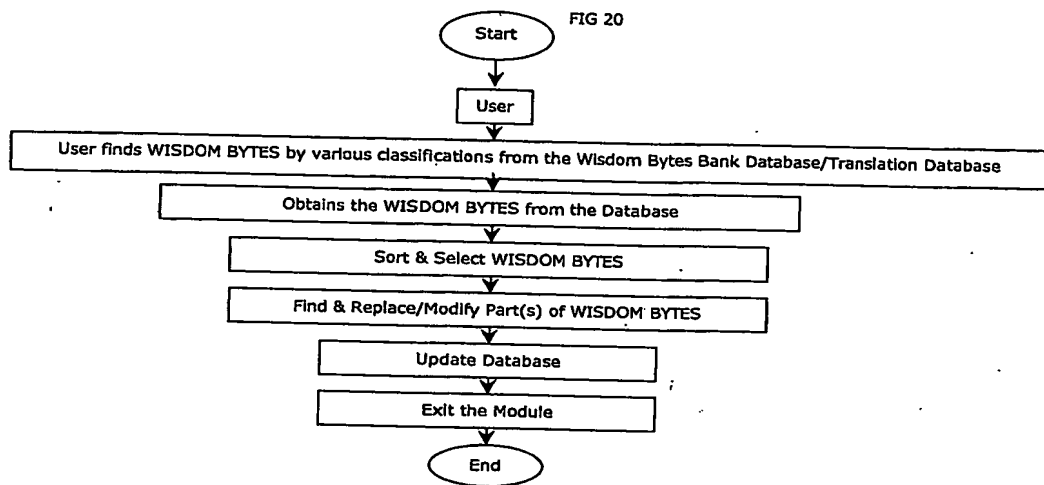


FIG 22

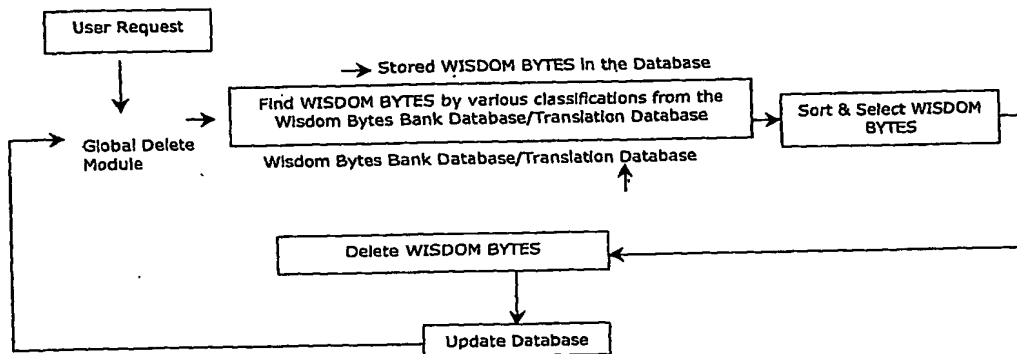
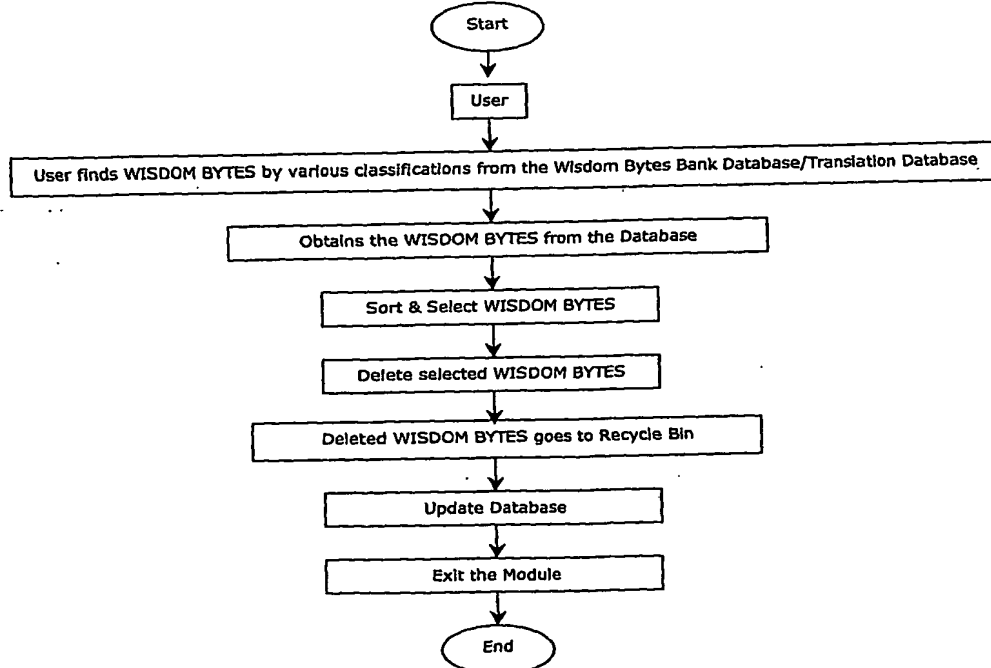


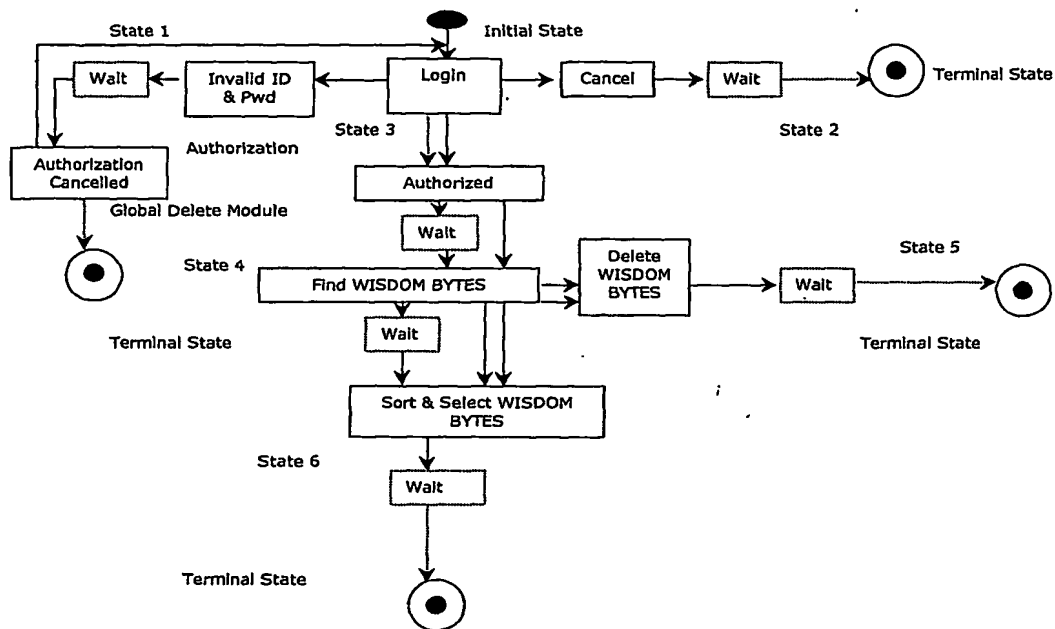
FIG 23





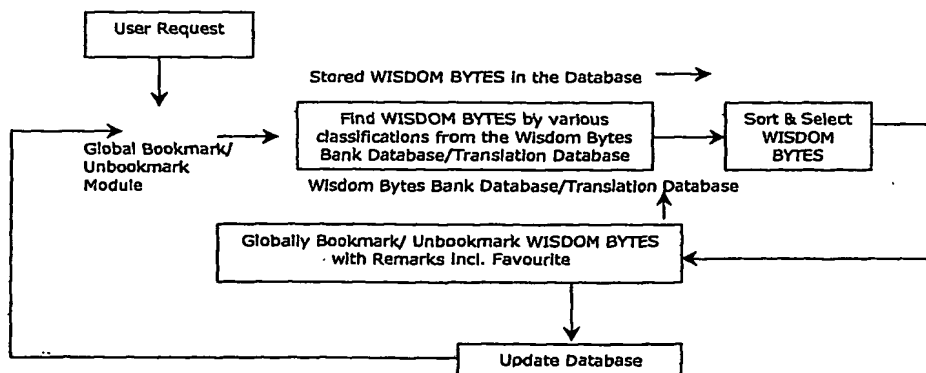
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FIG 24

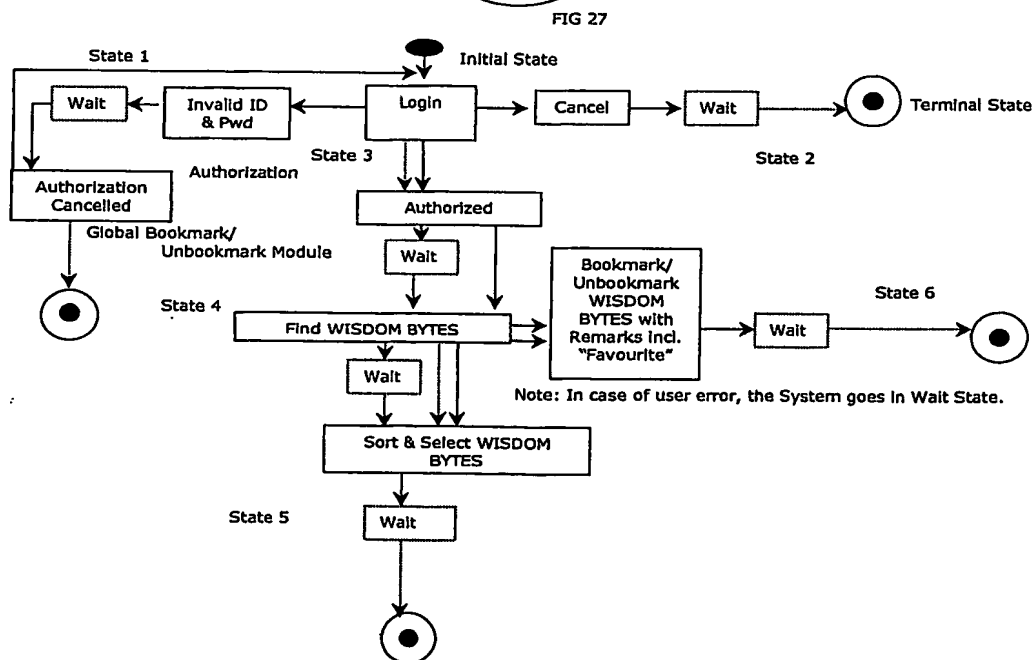
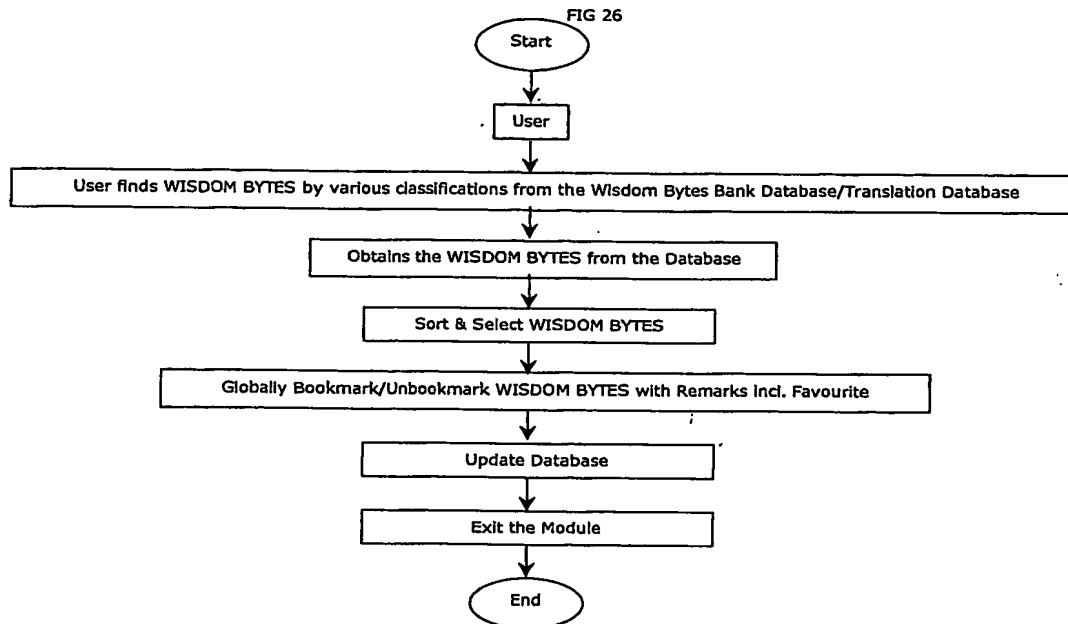


Note: In case of user error, the System goes in Wait State.

FIG 25



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FIG 28

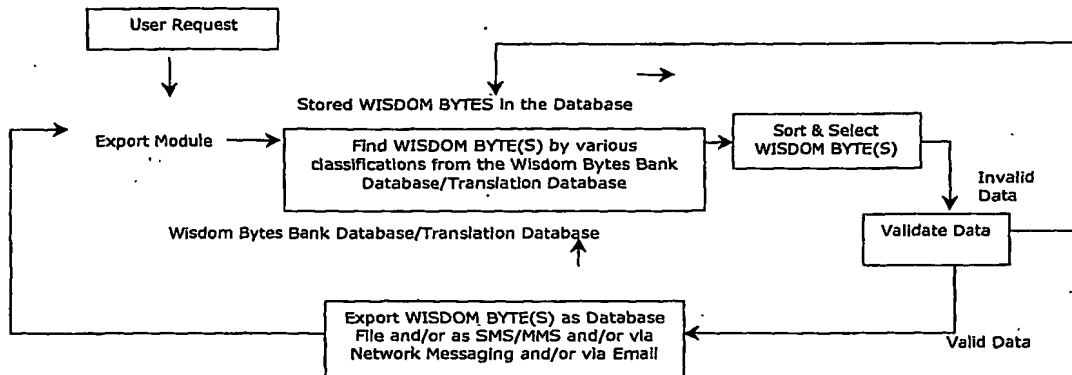
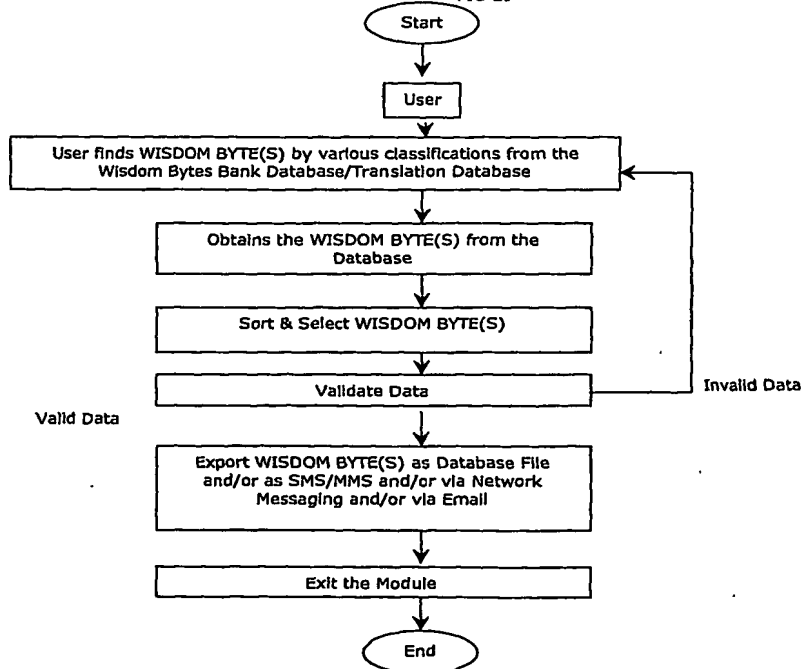
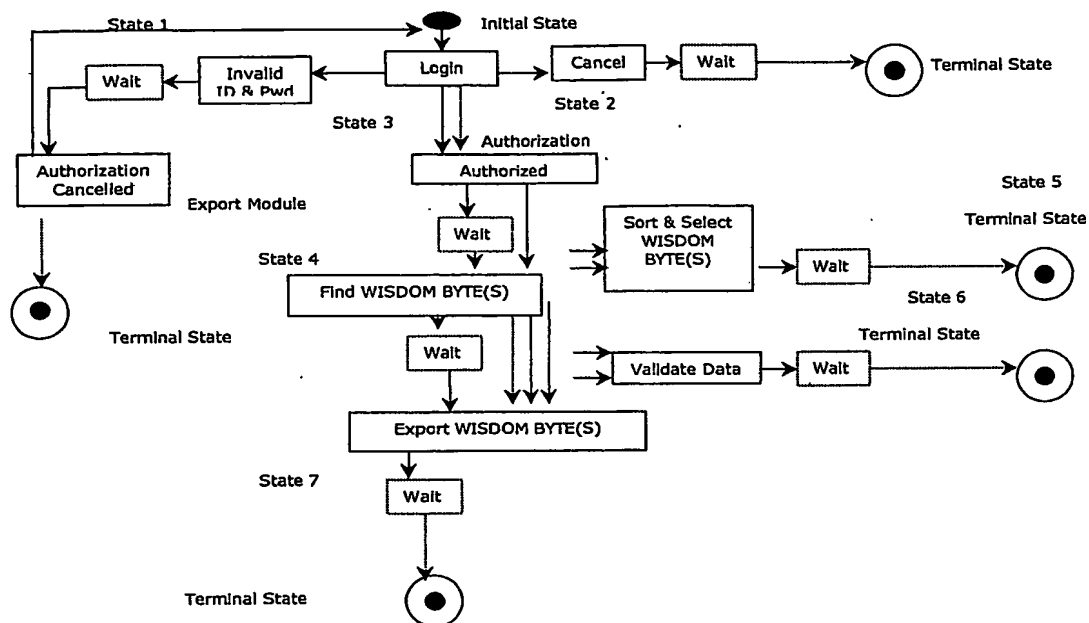


FIG 29



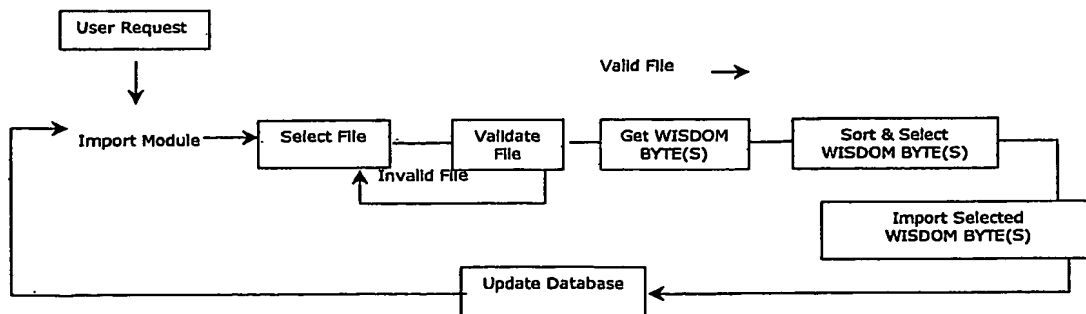
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FIG 30



Note: In case of user error, the System goes In Wait State.

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FIG 32

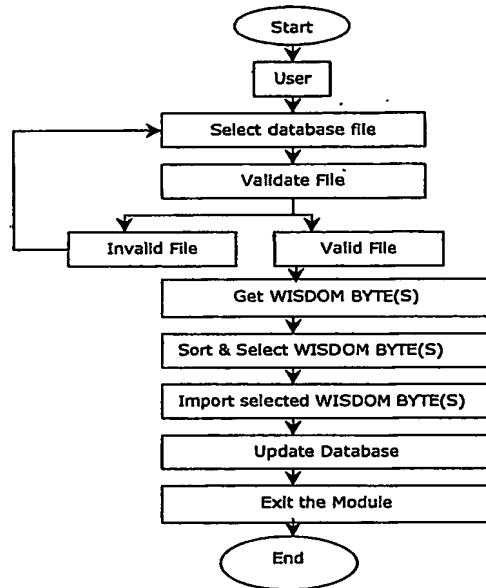
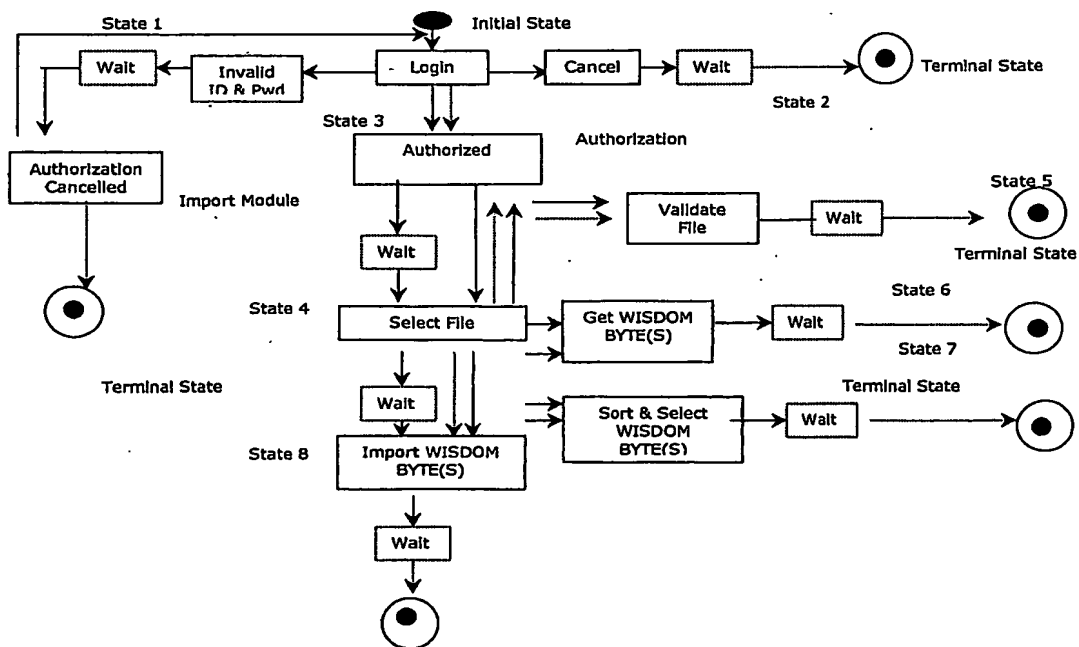


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FIG 34

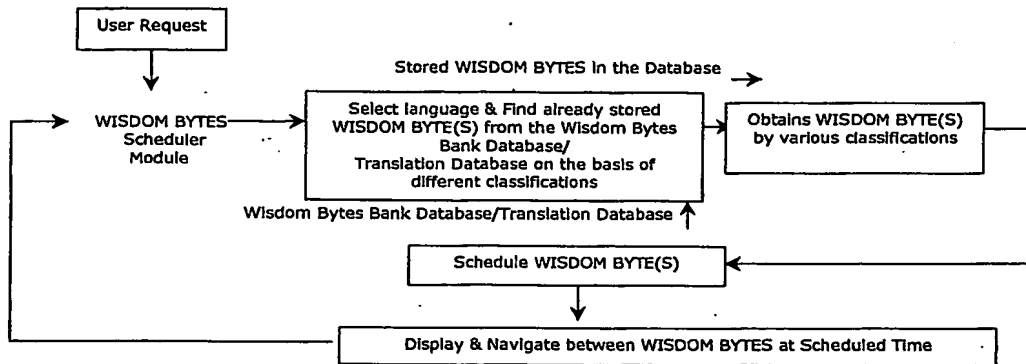
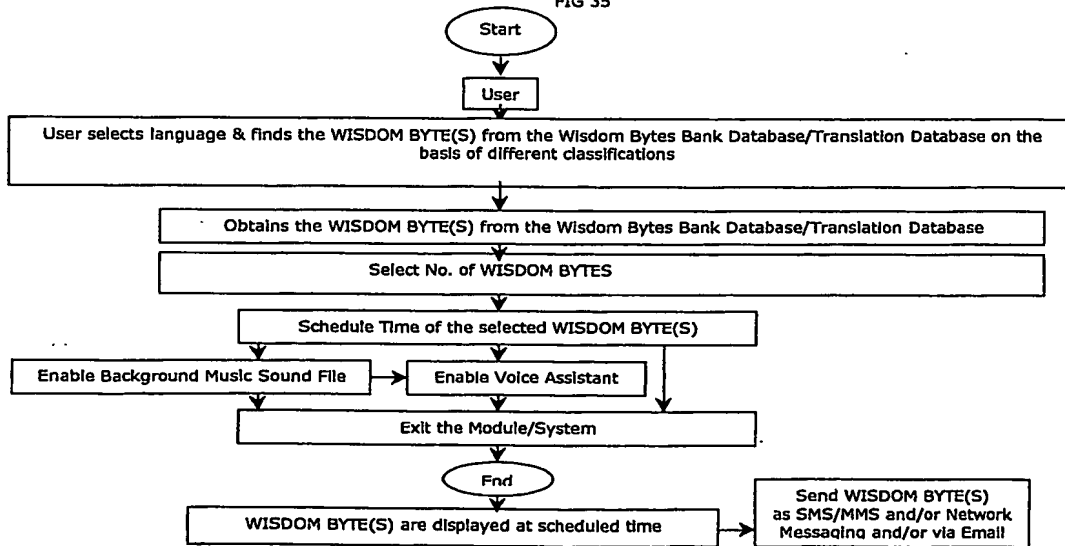


FIG 35



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FIG 36

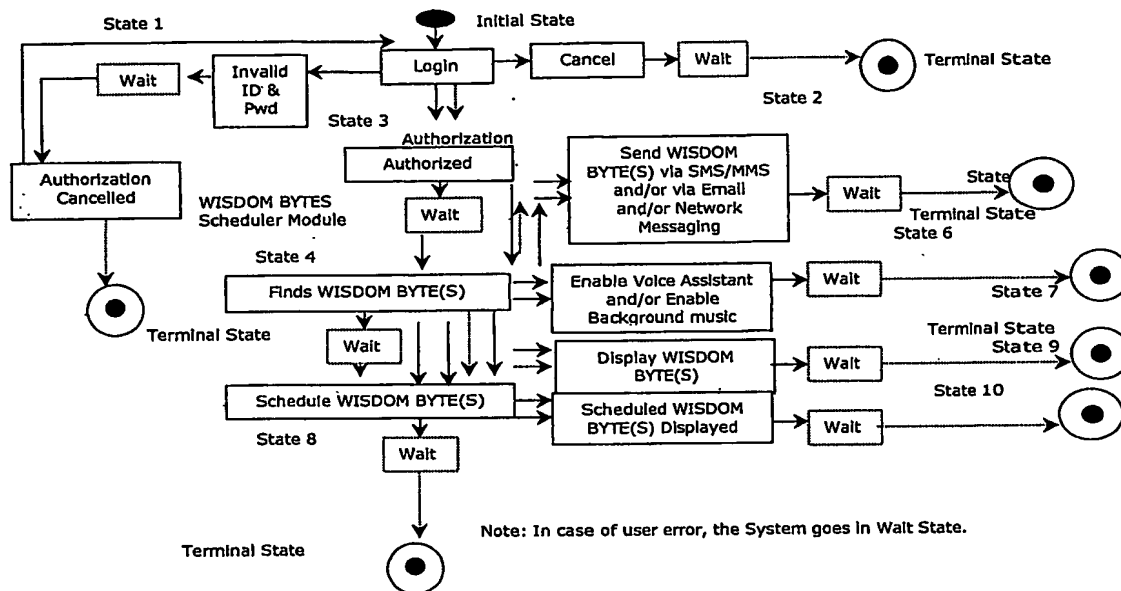
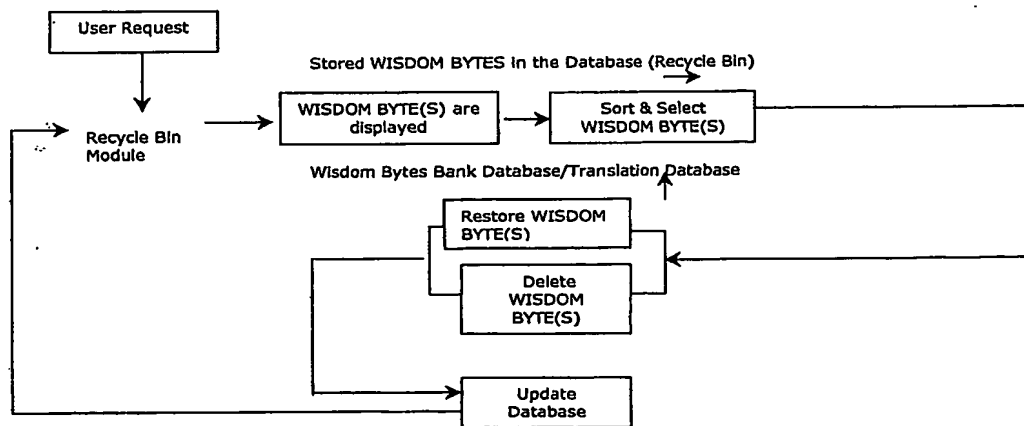
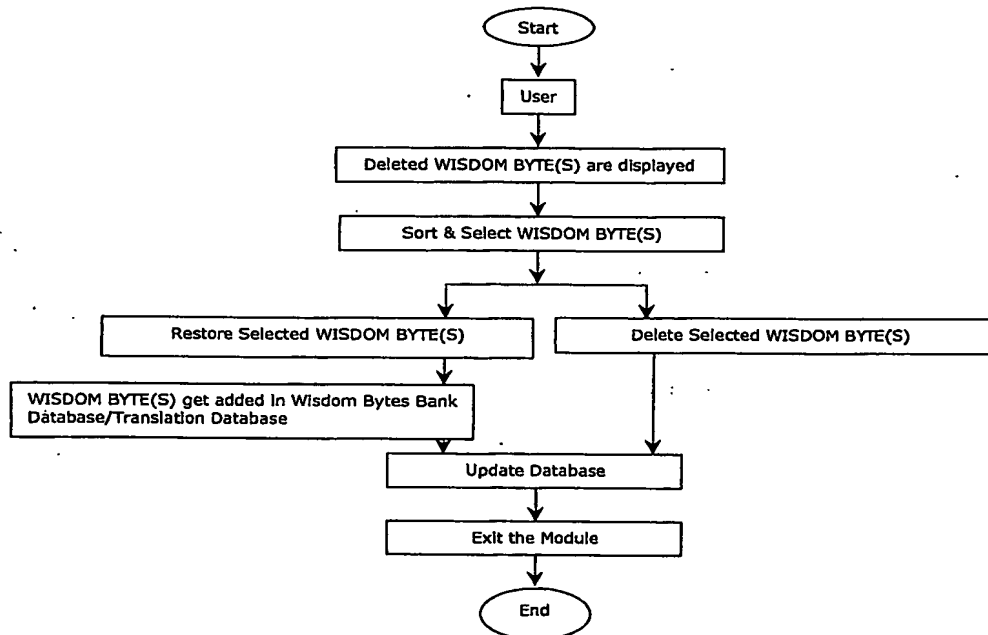


FIG 37



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FIG 38





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FIG 39

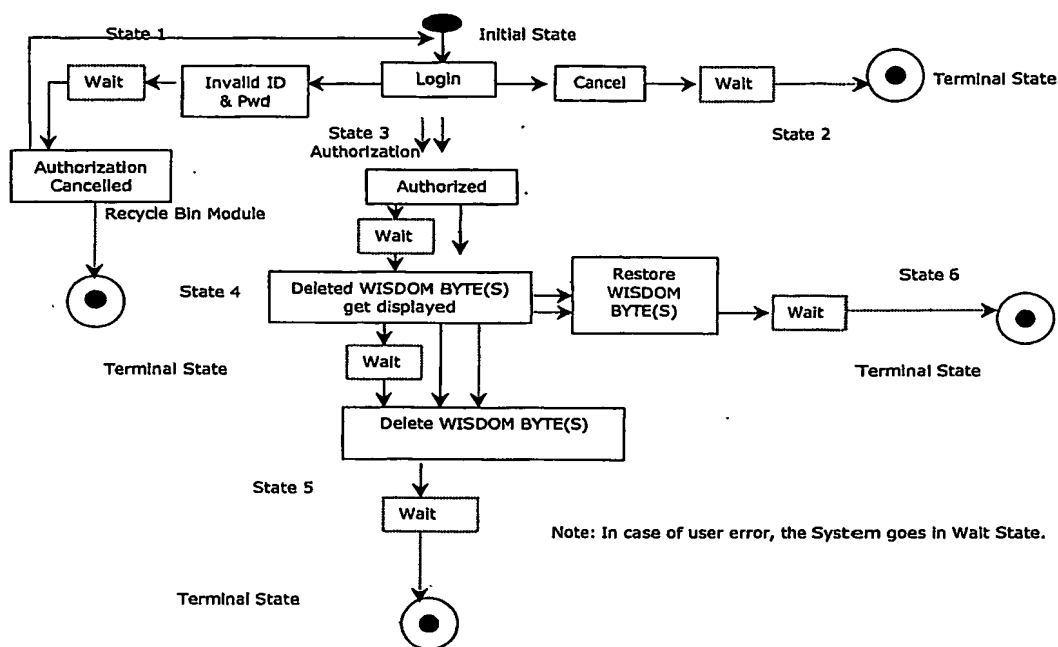
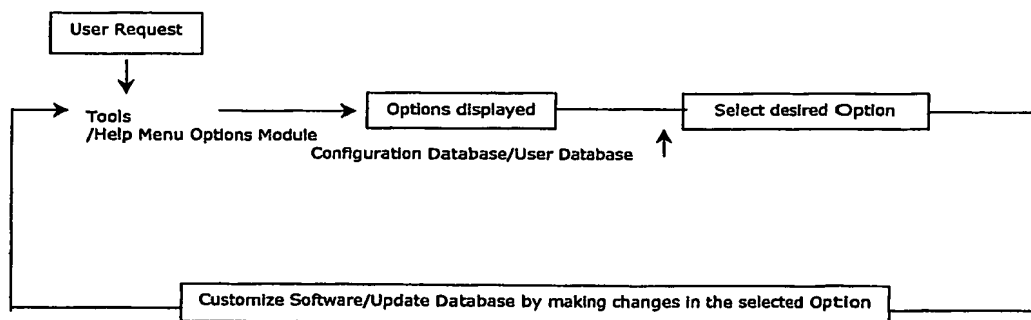
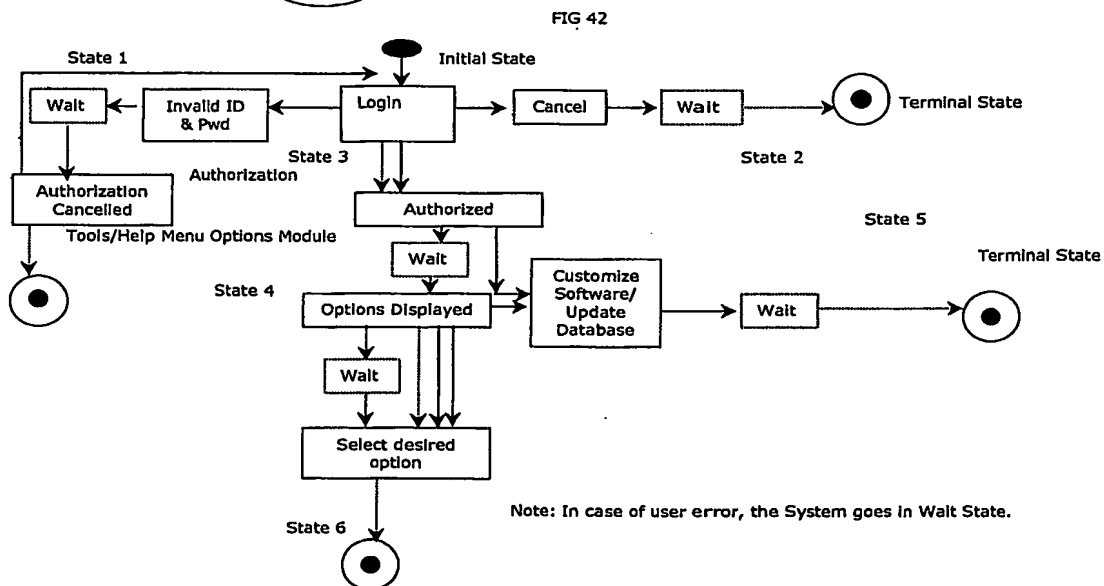
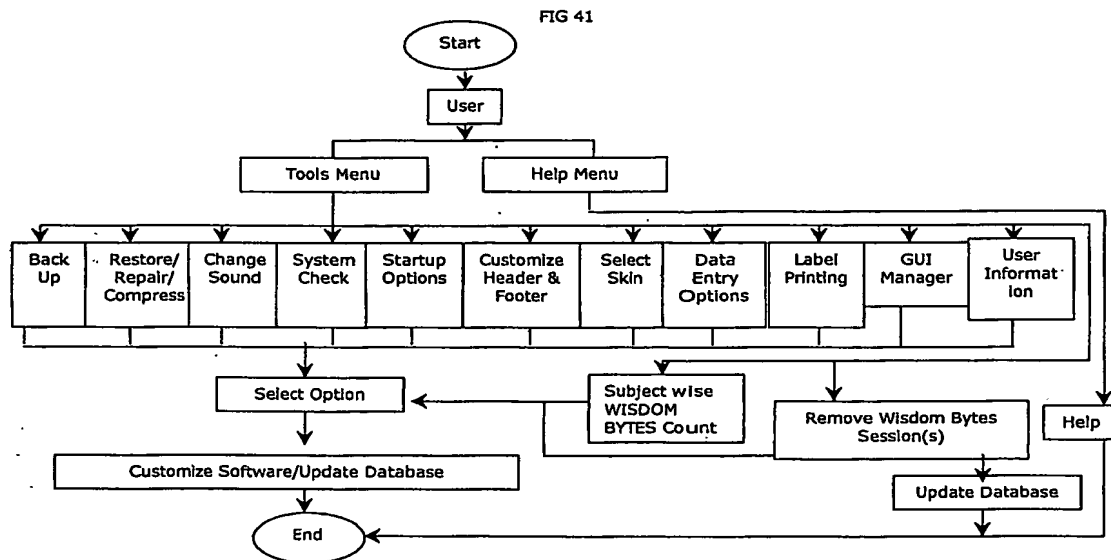


FIG 40



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FIG 43

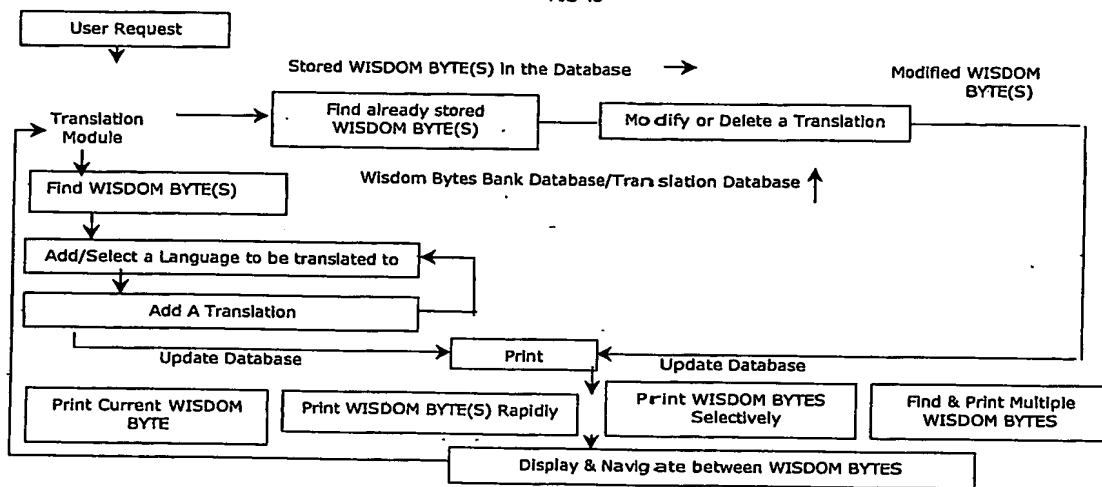
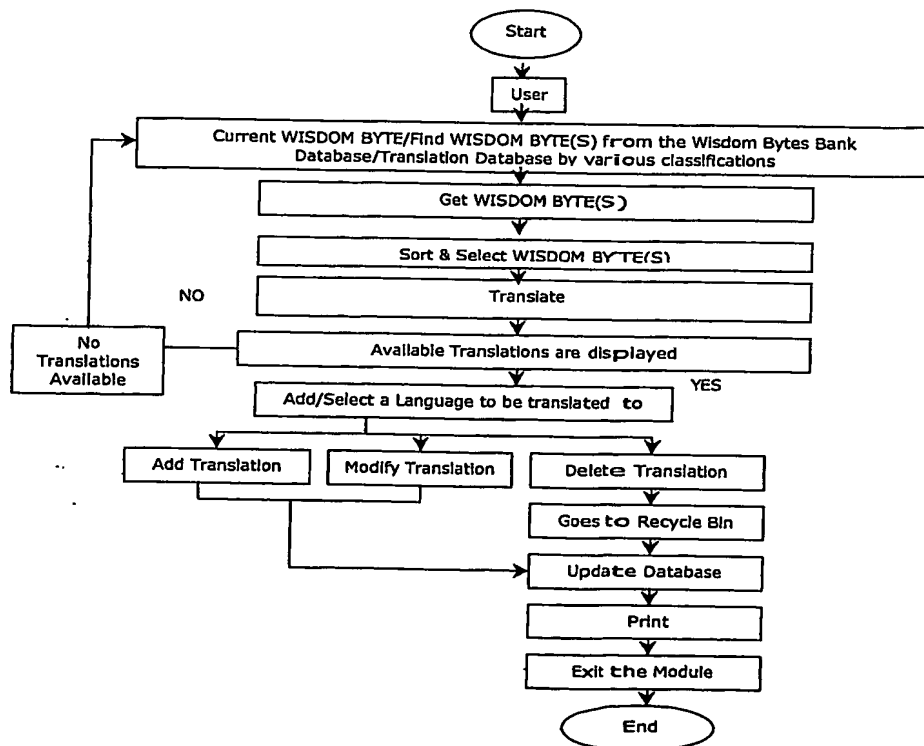


FIG 44



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FIG 45

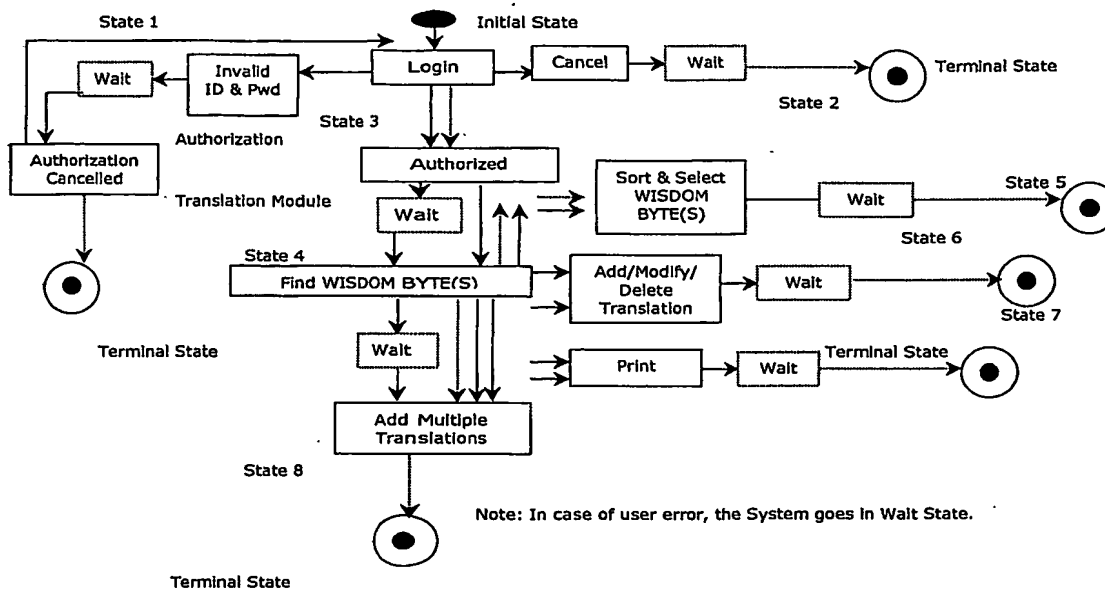
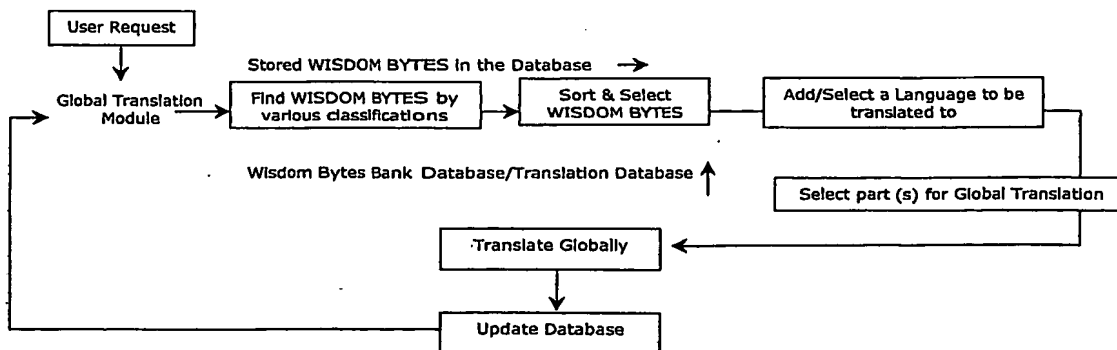


FIG 46



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FIG 47

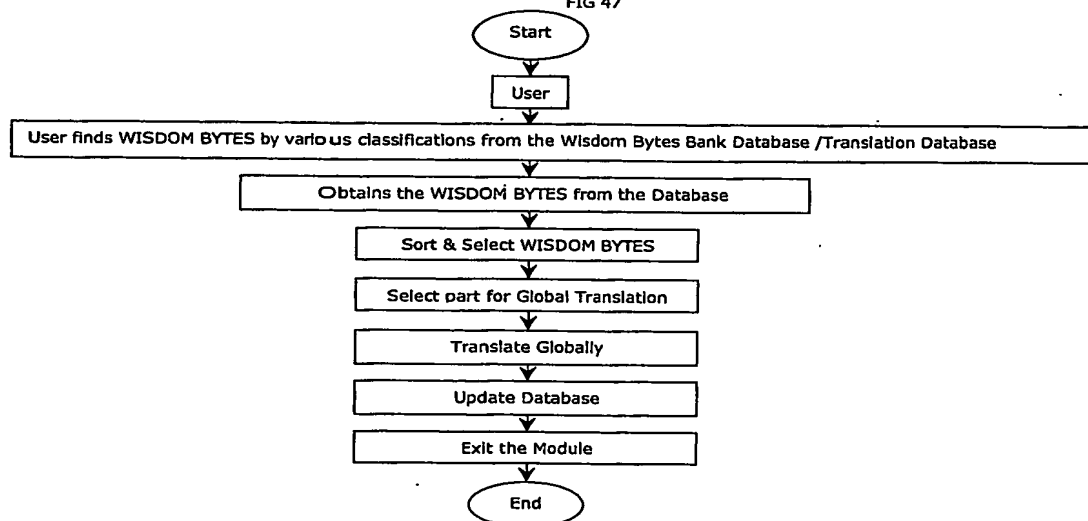
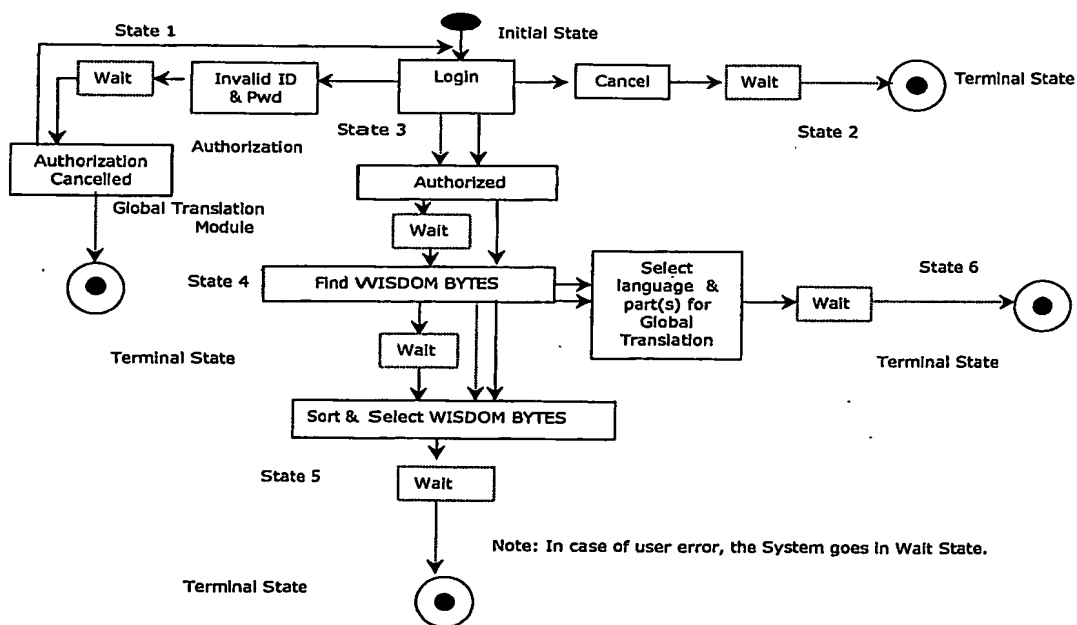


FIG 48



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FIG 49

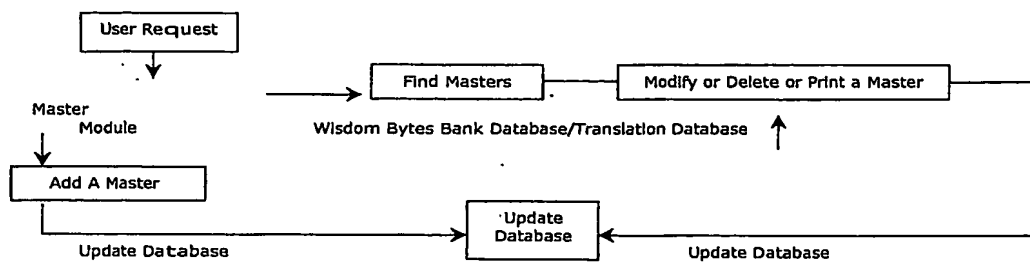
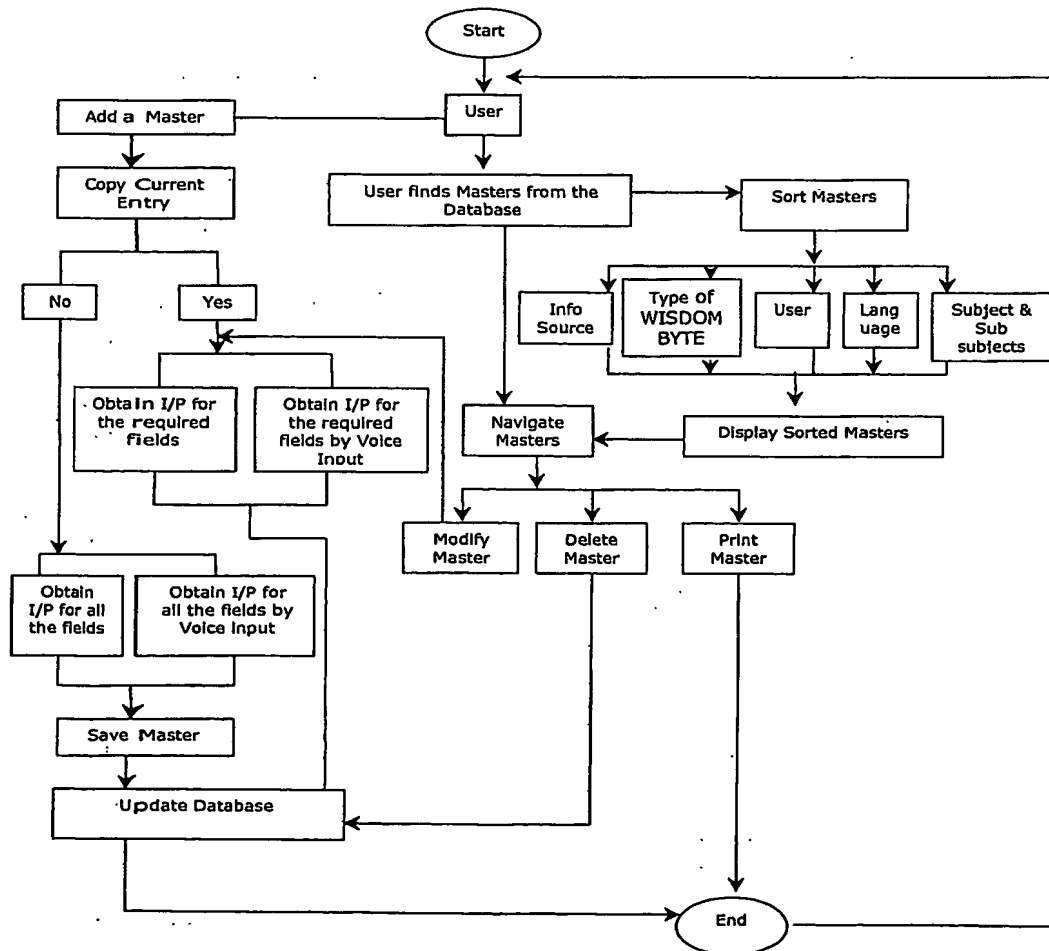
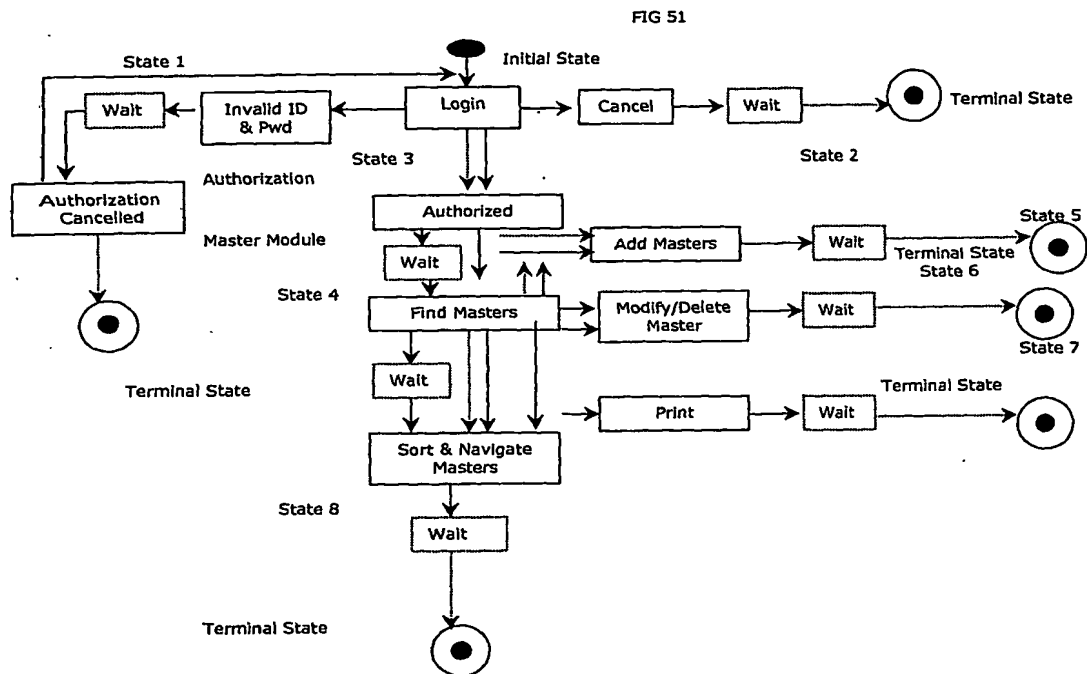


FIG 50



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Note: In case of user error, the System goes In Wait State.